



Attorney Docket No. 0756-2417

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 10/034,498

Filed: January 3, 2002

For: METHOD OF MANUFACTURING
SEMICONDUCTOR DEVICE

) Group Art Unit: 2824

) Examiner: B. Smith

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is being
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Adeline M. Stamp

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

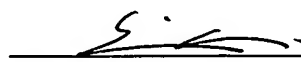
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

This Information Disclosure Statement is being submitted before the issuance of a first Office Action on the merits, therefore, no fee is required.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285
Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789

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